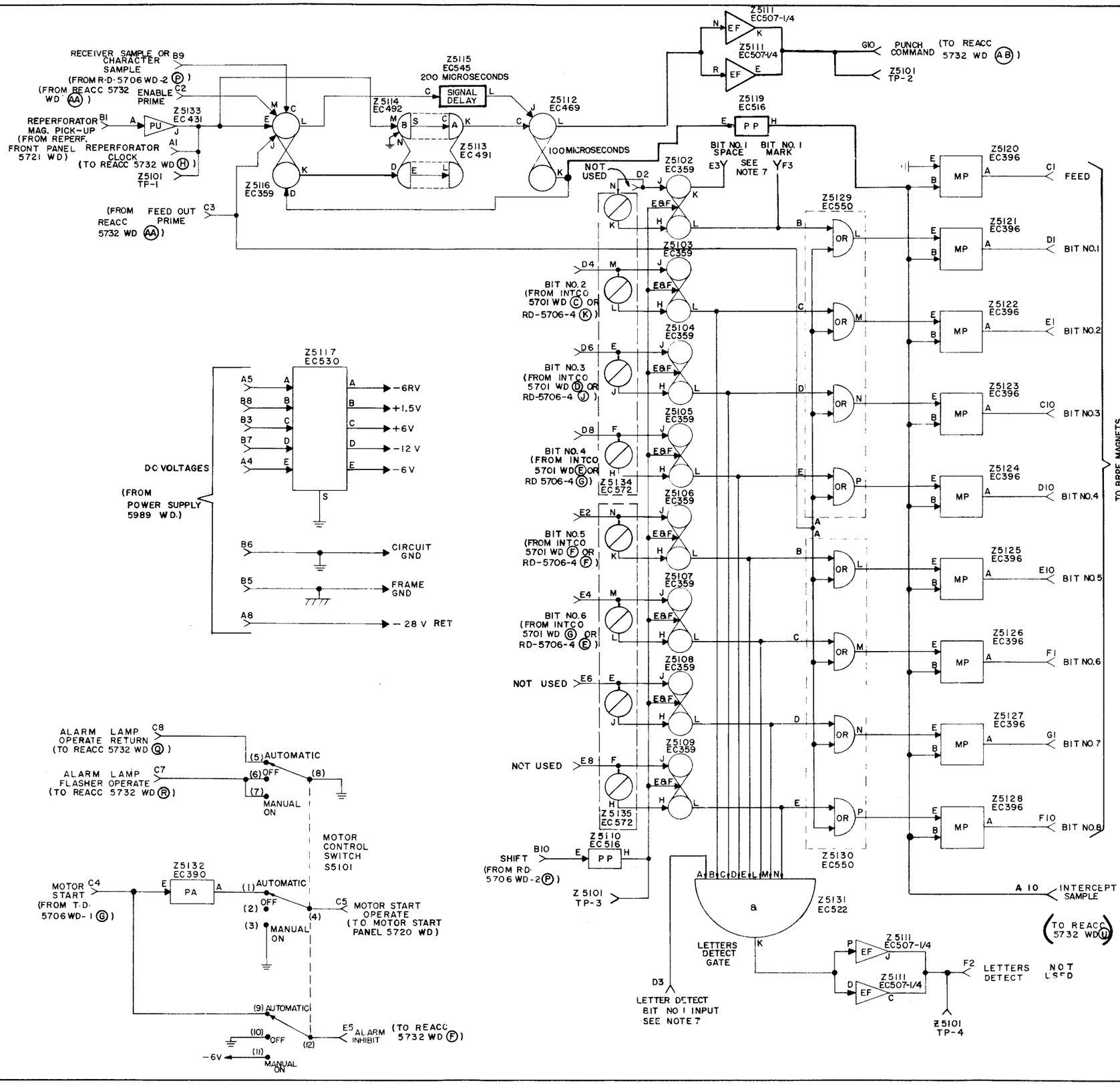
NO	NOTES
1.	REFER TO 5759 WD FOR ACTUAL WIRING DIAGRAM.
2.	ALL VOLTAGES DC UNLESS OTHERWISE SPECIFIED.
3.	TERMINAL DESIGNATION ENCLOSED IN PARENTHESIS ARE FOR REFERENCE ONLY AND NOT MARKED ON COMPONENT.
4.	SPARE CIRCUITS AVAILABLE: INPUT F AND OUTPUT R ON CIRCUIT CARDS Z 5129, EC550 & Z 5130, EC550 NOT USED.
5.	REFER TO SPECIFICATION FOR ADJUSTMENT OF VARIABLE TIMING CIRCUITS.
6.	>—— INDICATES TERMINAL ON RECEPTACLE J 5101, UNLESS OTHERWISE SPECIFIED.
7	FOR 5 LEVEL CODE OPERATION D3 IS STRAPPED TO E3. FOR 8 LEVEL CODE OPERATION D3 IS STRAPPED TO F3
8.	THE +28V. LINE IS EXTERNALLY STRAPPED TO GROUND.
9.	SIGNALS GOING FROM ONE WD TO ANOTHER WHICH ARE CONNECTED TOGETHER HAVE THE SAME TITLE ON EACH WD.



5758 WD

REVISIONS

ISSUE	DATE	AUTH. NO.
~~1~~ 2	1-24-64	79835
~~2~~ 3	4-7-64	79849
4	8-31-64	82490
5	10-4-65	88570
6	4-18-66	90307

SCHEMATIC WIRING DIAGRAM FOR HIGH SPEED REPERFORATOR CONTROL 5 HISRECO 5

ASSEM. NO. 193164

APPROVALS

D AND R	E OF M

E-NUMBER

PROD. NO. 5758 WD

DATE 7—6—63

P.D. FILE NO. 33—A25AA

DRAWN RDL	CHKD.
ENGD. REP	APPD.

TELETYPE CORPORATION

5758WD